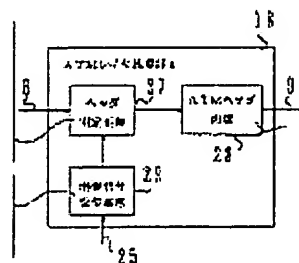
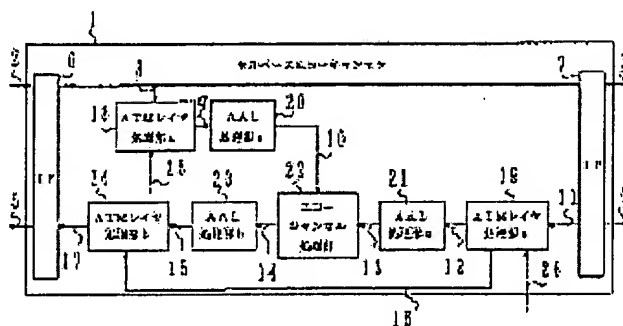


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PURPOSE: To apply a cell base echo canceller to an arbitrary line through which arbitrary communication information is transmitted and received by conducting the echo cancel processing of each channel in accordance with an external control signal. **CONSTITUTION:** A control signal reception processing part 29 of an ATM layer processing part 18 monitors reception of a control signal 25 which instructs bypass execution/release of echo cancel processing. When the execution indication is received, the execution object channel number is reported to a header discrimination processing part 27, and the processing part 27 collects channel information from the header of a copy 8 of an inputted far-end speaker signal 2. Coincidence or noncoincidence with the designated channel from the processing part 29 is discriminated. That is, the processing part 27 uses the header of the arriving cell to retrieve a table of the processing part 29 and refers to the part of a pertinent VPI/VCi to discriminate whether the echo canceller is necessary or not. If it is unnecessary, any processings of the cell are not performed and the cell is abandoned; but if it is necessary, the cell is processed as sample data in an ATM header processing part 28 to output the part except the ATM header.



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